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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/791,239	03/02/2004	Santosh P. Gaur	RPS920020016US1(4266)	3606

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EXAMINER

PERUNGAVOOR, VENKATANARAY

ART UNIT

PAPER NUMBER

2132

MAIL DATE

DELIVERY MODE

09/15/2008

PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

### Office Action Summary

**Application No.**

10/791,239

**Applicant(s)**

GAUR ET AL.

**Examiner**

Venkat Perungavoor

**Art Unit**

2132

**Period for Reply** -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 7/2/2008.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-8, 10-18 and 20-27 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-8, 10-18 and 20-27 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-8508)
- 4) ☐ Interview Summary (PTO-413)
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: \_\_\_\_\_
- Paper No(s)/Mail Date \_\_\_\_\_

## **DETAILED ACTION**

### ***Response to Arguments***

Applicant's arguments with respect to claims 1-8, 10-19, 21-27 have been considered but are moot in view of the new ground(s) of rejection.

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1, 12, 23, are rejected under 35 U.S.C. 102(e) as being unpatentable over US Patent 2004/0202319 to Hussain et al.(hereinafter Hussain).

Regarding Claim 1, 12, 23, Hussain discloses

an input interface configured to receive data into the system, to receive control information, wherein a predetermined algorithm is based on the control information, and to forward the control information to a plurality of processors to perform respective cryptographic operations according to the predetermined algorithm see Fig. 5 item 505;

the plurality of processors in a cascaded arrangement, each processor having an input coupled to the input interface and an output coupled to respective inputs of each of the other processors downstream in the arrangement, the processors each configured to perform respective cryptographic operations on the data see par. Par. 0088;

wherein the plurality of processors comprises a first processor having an output coupled with a first input of a second processor and a first input of a third processor; wherein the first processor can be configured to compress uncompressed data and to decompress compressed data see Fig.4 item Hash Operations;

wherein the second processor comprises an output coupled with a second input of the third processor and can be configured to obscure non-secure data and decipher secure data input see Par. 0029;

wherein the third processor can be configured to hash the data see Par. 0058;

and an output interface coupled to the input interface and to the output of each of the processors, the output interface configured to transmit data out of the system and to direct the data through the system in coordination with the input interface according to the predetermined algorithm see Fig. 5 item 506.

### ***Claim Rejections - 35 USC § 103***

Claims 2-7 13-18, 24-25, 27, are rejected under 35 U.S.C. 103(a) as being unpatentable over US Patent 2004/0202319 to Hussain et al.(hereinafter Hussain) in view of US Patent 6240513 to Friedman et al.(hereinafter Friedman).

Regarding Claim 2-3, 13-14, Hussain does not disclose the first processor coupled to input interface for compressing and decompressing data. However, Freidman discloses the processor coupled to the input interface for compressing and decompressing data see Fig. 11 item 1110 using a Lempel-Ziv-Stac(LZS) compression algorithm see Col 15 Ln 63-65. It would be obvious to one having ordinary skill in the art at the time of the invention to include compression and decompression of data in the invention of Ober in order to make payload of packet be transmitted at a faster rate.

Regarding Claim 4-5, 15-16, Hussain discloses the encryption block for encrypting the data using DES, 3DES see Fig. 3 item 309.

Regarding Claim 6-7, 17-18, Hussain discloses the hash block used to check integrity using MD5, SHA-1 see Fig. 4 item Finished Operation.

Regarding Claim 24-25, Hussain discloses the encryption block for encrypting the data using DES, 3DES see Fig. 3 item 309; the hash block used to check integrity using MD5, SHA-1 see Fig. 4 item Finished Operation. But, Hussain does not disclose the first processor coupled to input interface for compressing and decompressing data.

However, Freidman discloses the processor coupled to the input interface for compressing and decompressing data see Fig. 11 item 1110 using a Lempel-Ziv-Stac(LZS) compression algorithm see Col 15 Ln 63-65. It would be obvious to one having ordinary skill in the art at the time of the invention to include compression and decompression of data in the invention of Hussain in order to make payload of packet be transmitted at a faster rate.

Regarding Claim 27, Hussain does not disclose the checksum. However, Friedman discloses the checksum see Par. 0091.

Claims 8-9,19-20, 26, are rejected under 35 U.S.C. 103(a) as being unpatentable over US Patent 2004/0202319 to Hussain et al.(hereinafter Hussain) in view of US Patent 6901516 to Howard et al.(hereinafter Howard).

Regarding Claim 8-9, 19-20, 26, Hussain does not discloses the control information being used to forward information is performing cryptographic operations. However, Howard discloses a controller being used to forward information and controlling operations of units see Fig. 3 item "Controller". It would be obvious to one having ordinary skill in the art at the time of the invention to include a controller to control units

in the invention of Hussain in order to have centralized control and data that is fed on the bus being direct to known units.

Claims 10, 21, are rejected under 35 U.S.C. 103(a) as being unpatentable over US Patent 2004/0202319 to Hussain et al.(hereinafter Hussain) in view of US Patent 6901516 to Howard et al.(hereinafter Howard) further in view of US Patent 6240513 to Friedman et al.(hereinafter Friedman).

Regarding Claim 10, 21, Hussain nor Howard disclose the header containing information regarding the identity of algorithms used and the key information and lifetime. However, Friedman disclose the header containing information regarding algorithms and key information see Fig. 10 item 1030. It would be obvious to one having ordinary skill in the art at the time of the invention to include the identity of algorithms used and the key information and lifetime in the invention of Hussain in order to have known protocols used.

### ***Conclusion***

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a). A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the

shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Venkat Perungavoor whose telephone number is (571)272-7213. The examiner can normally be reached on 8:30-5:00. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Gilberto Barron can be reached on 571-272-3799. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/V. P./  
Examiner, Art Unit 2132  
September 10, 2008

/Gilberto Barron Jr/  
Supervisory Patent Examiner, Art Unit 2132